

	T1	T2	T3	T4	T5	T6	T7	T8	T9	T10
INSTR. 1	IF	RD	EXE	MEM	WB					
INSTR. 2		IF	RD	EXE	MEM	WB				
INSTR. 3			IF	RD	EXE	MEM	WB			
INSTR. 4				IF	RD	EXE	MEM	WB		
INSTR. 5					IF	RD	EXE	MEM	WB	
INSTR. 6						IF	RD	EXE	MEM	WB

Fig. 1

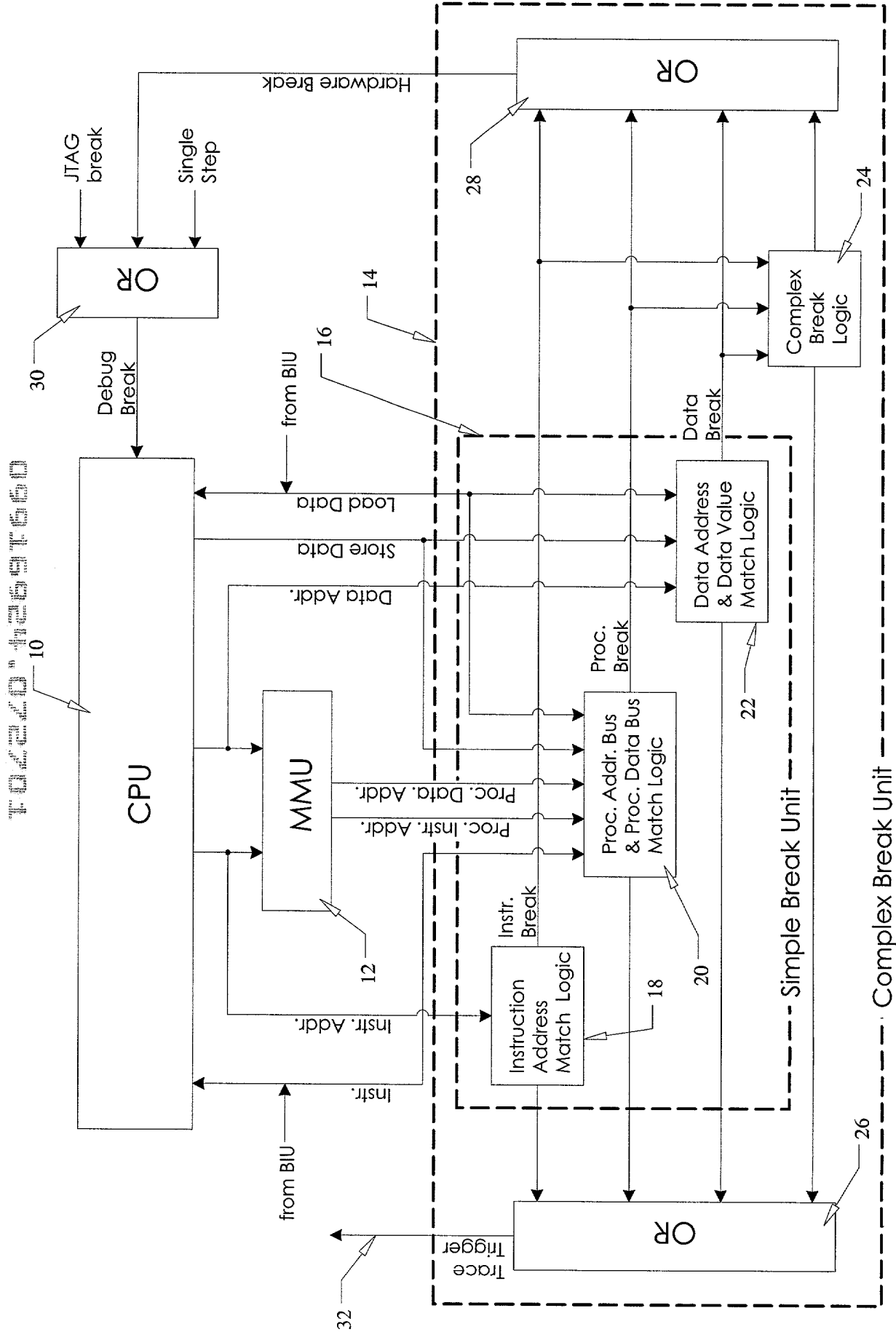


Fig. 2

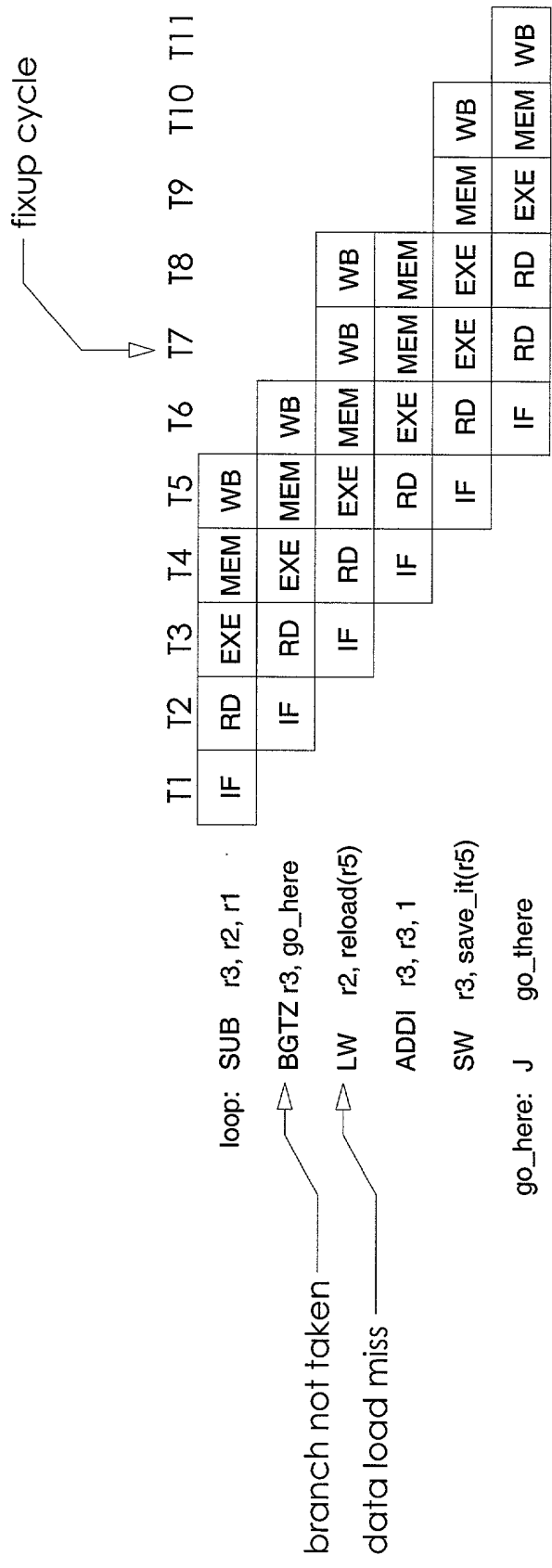


Fig. 3

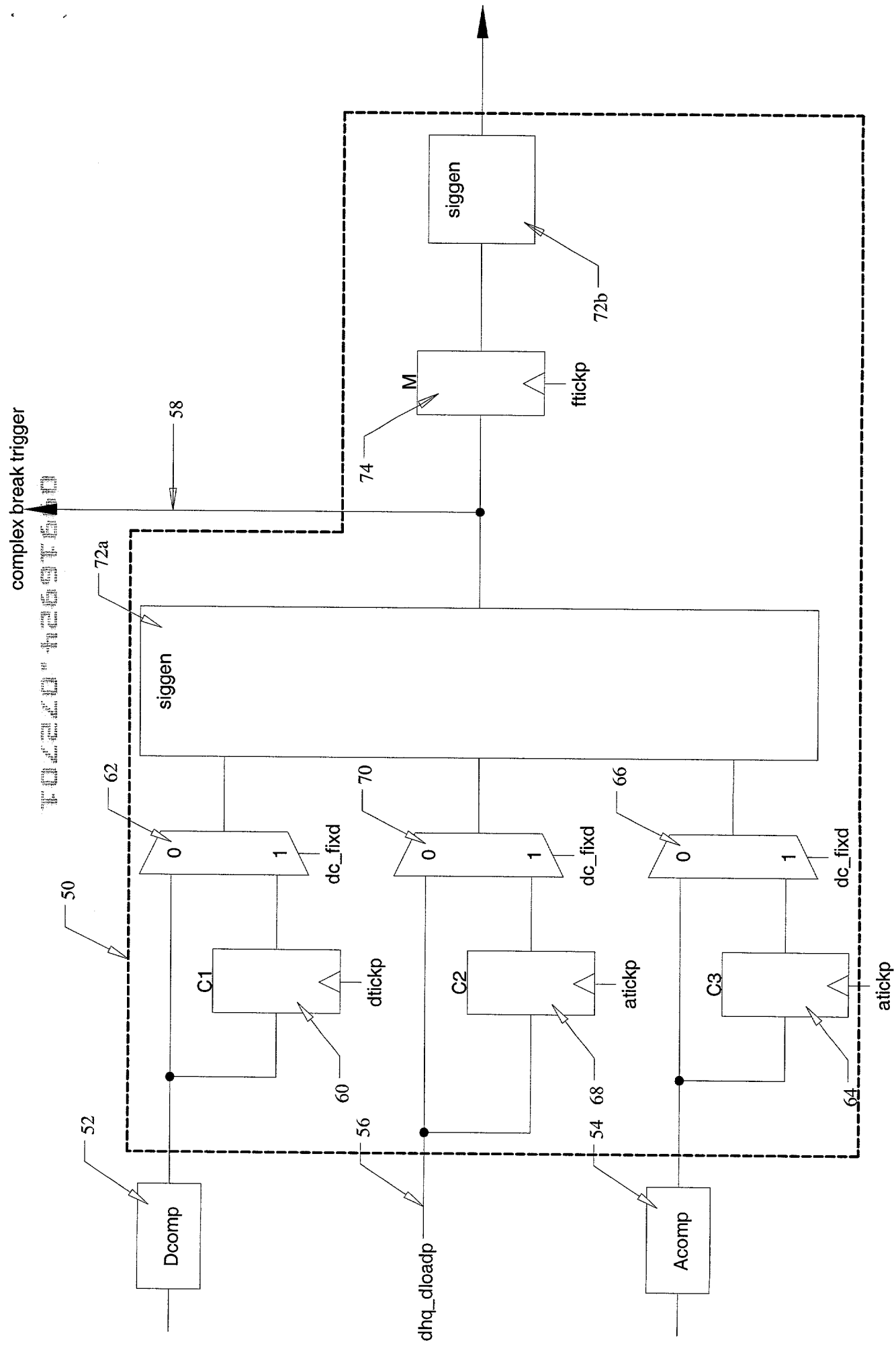


Fig. 4

